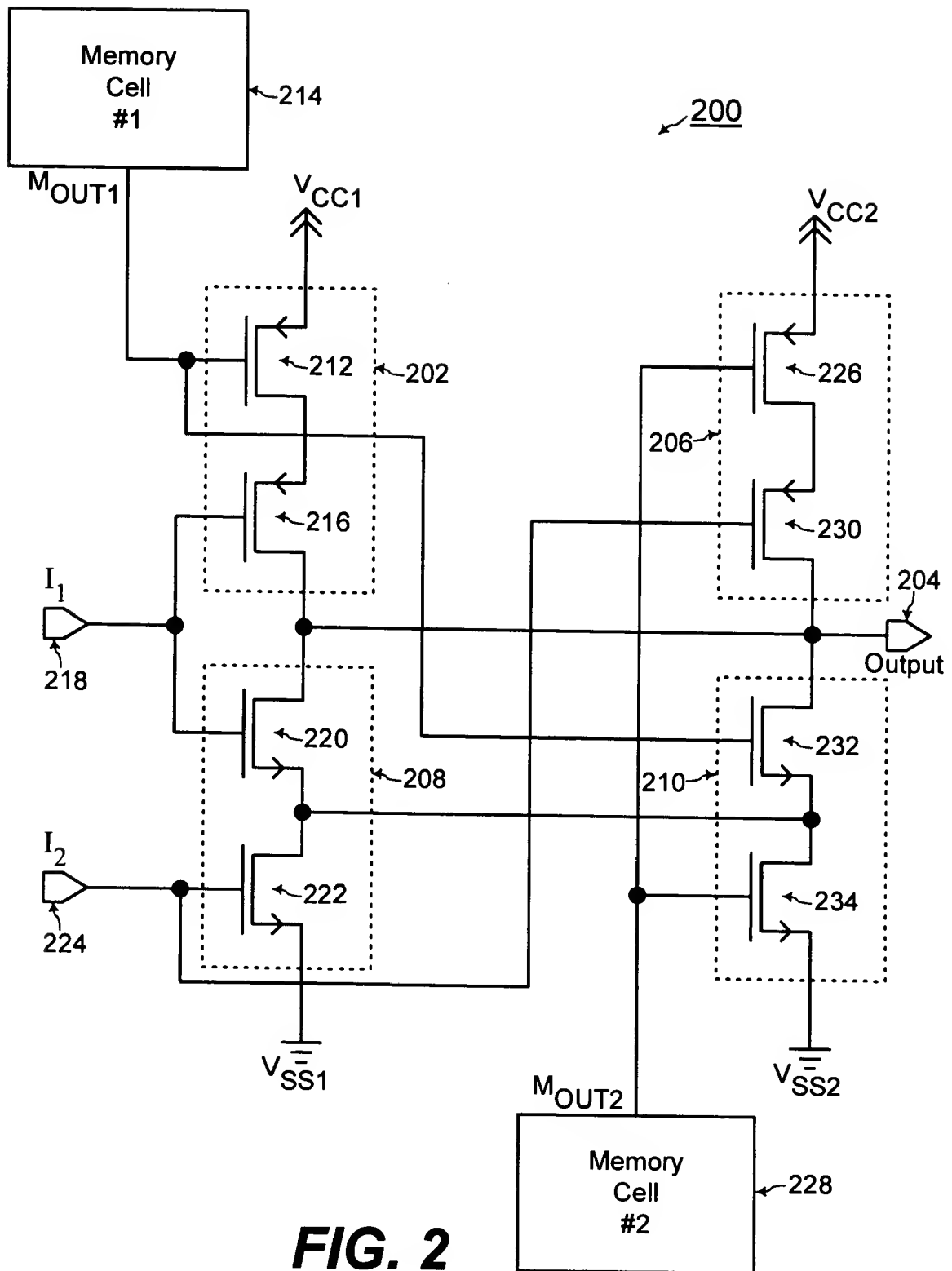


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FIG. 1 (Prior Art)

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**FIG. 2**

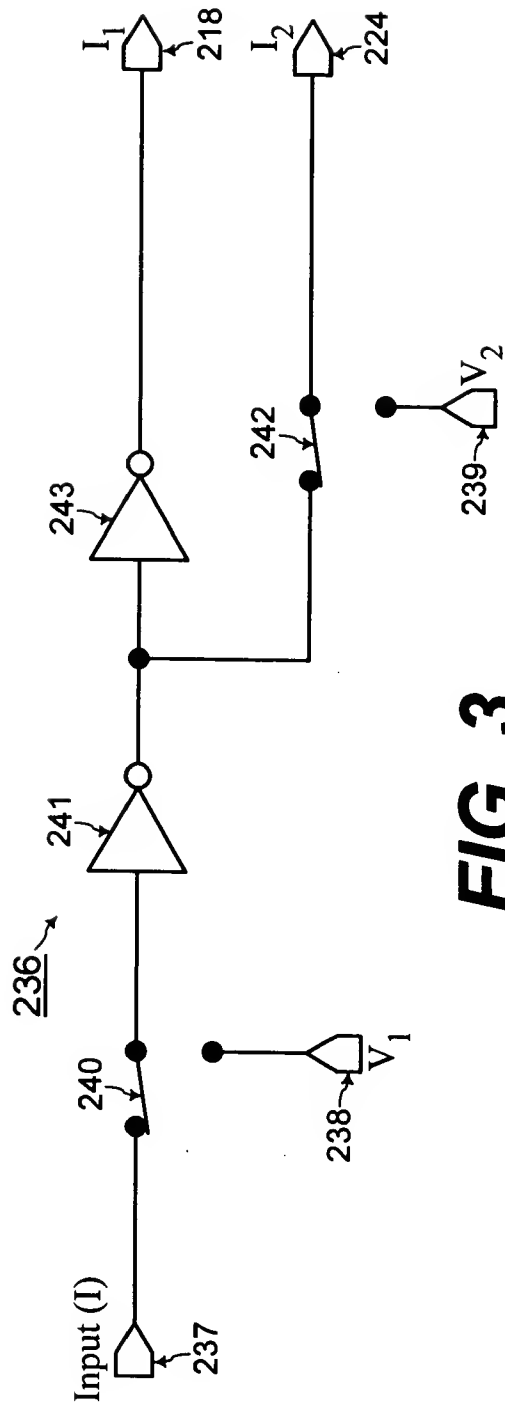


FIG. 3

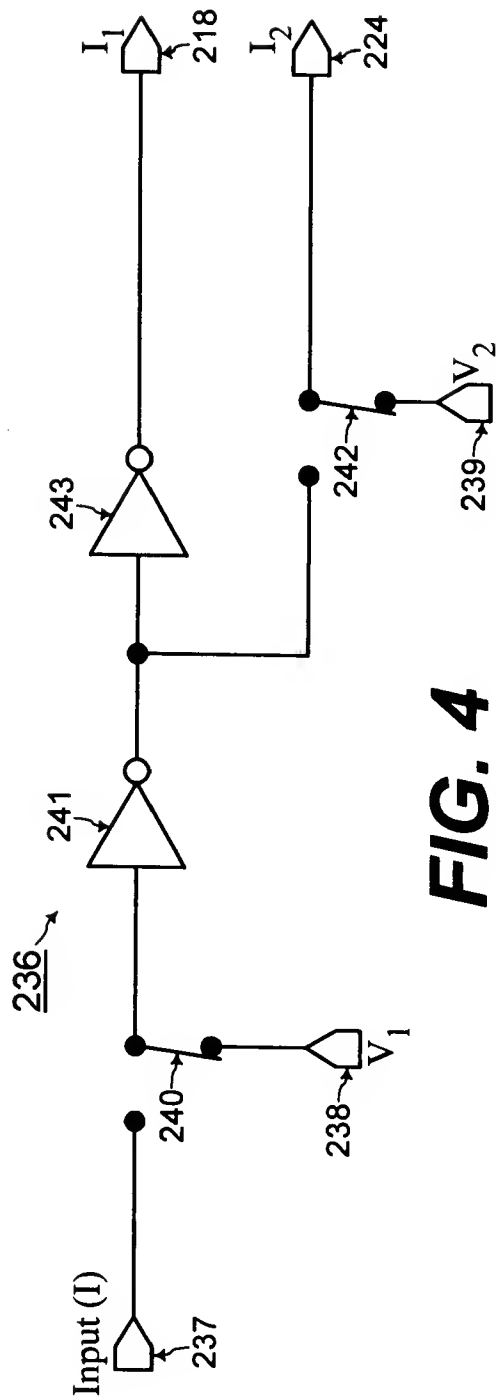


FIG. 4

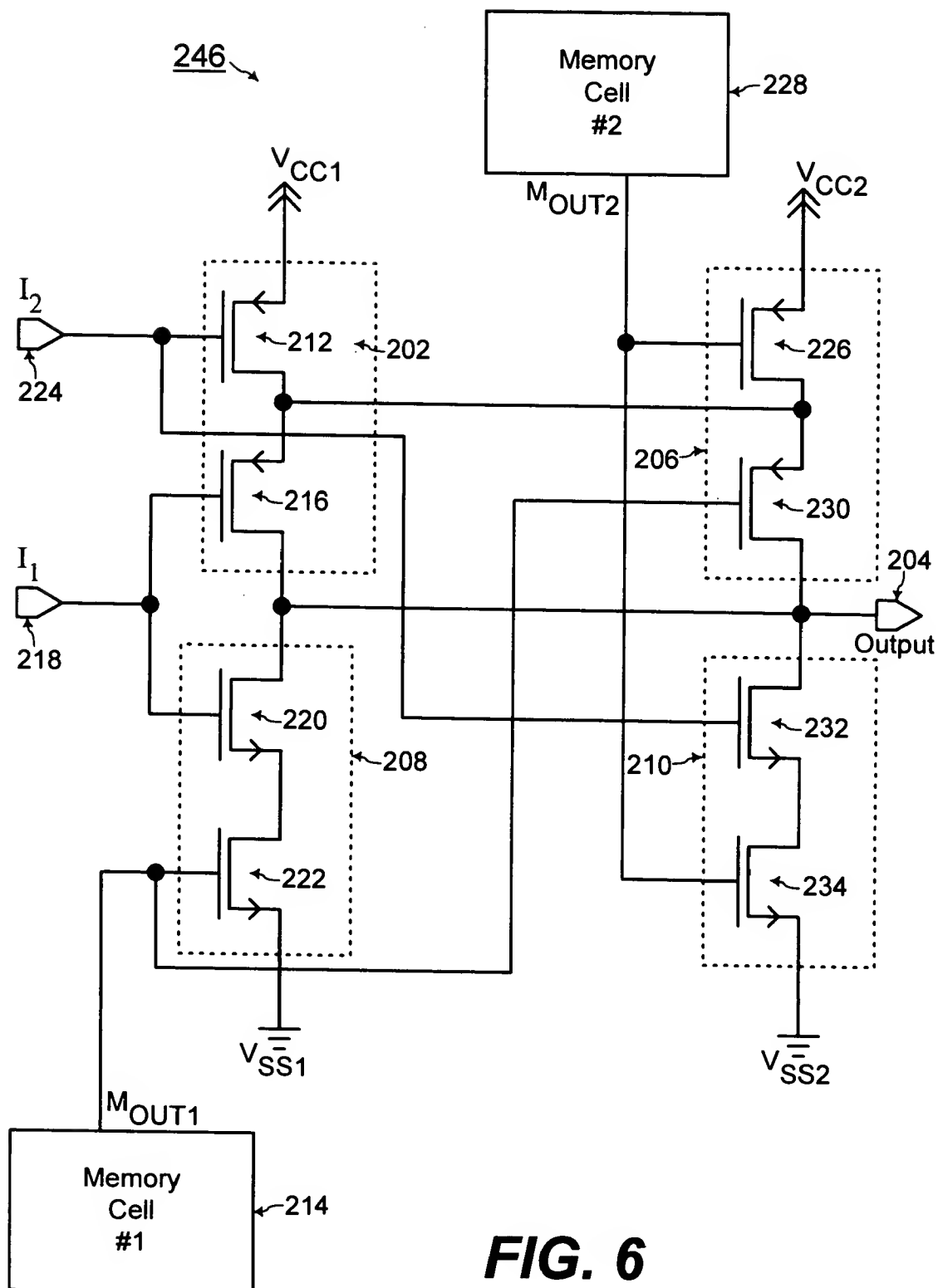
TOTAL = 245 - 11401

I_1	I_2	M_{OUT1}	M_{OUT2}	Output
0	0	0	0	1
0	0	0	1	1
0	0	1	0	1
0	0	1	1	0
0	1	0	0	1
0	1	0	1	1
0	1	1	0	0
0	1	1	1	0
1	0	0	0	1
1	0	0	1	0
1	0	1	0	1
1	0	1	1	0
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	0

244

251

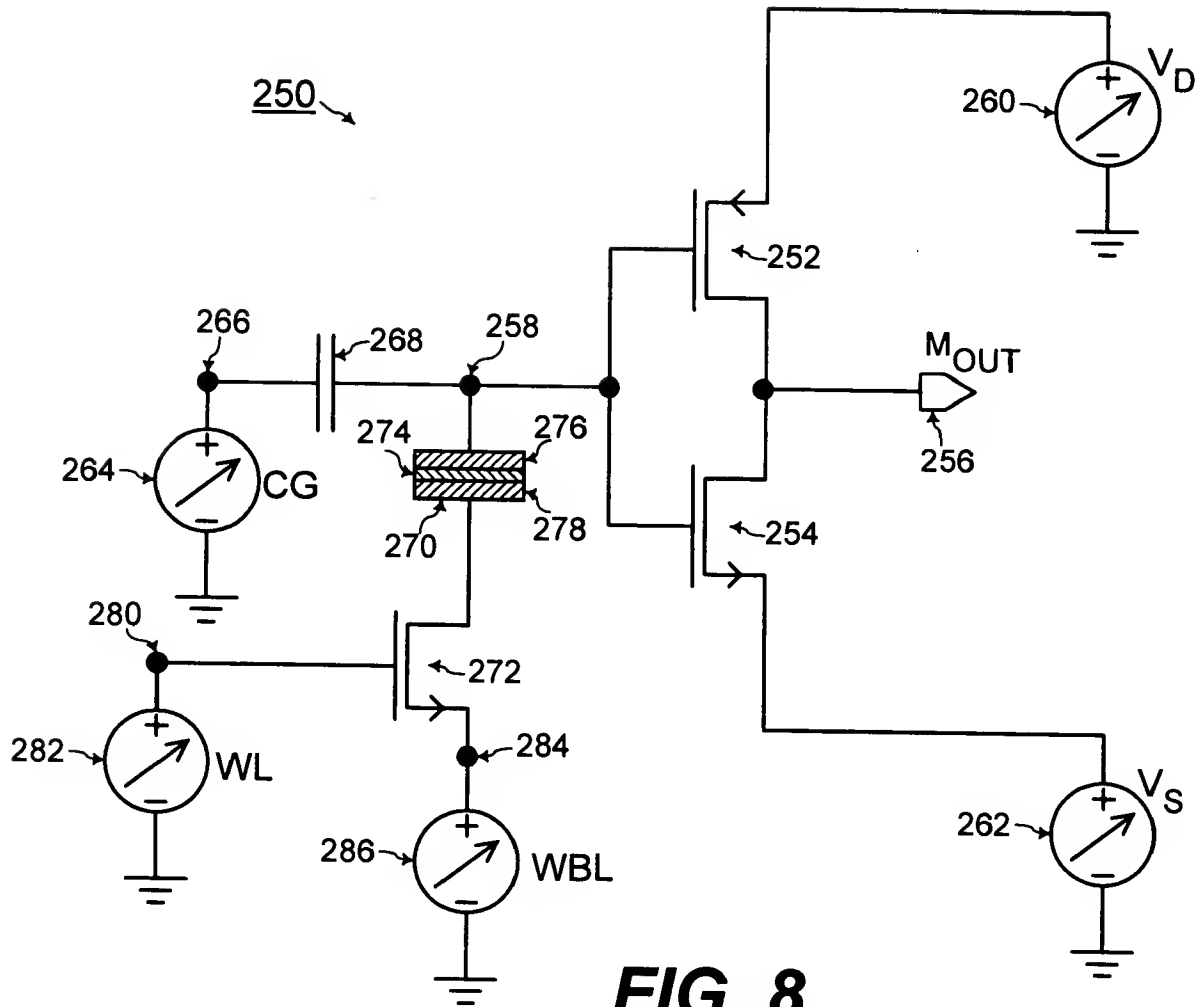
FIG. 5

**FIG. 6**

0991245-11401

I_1	I_2	M_{OUT1}	M_{OUT2}	Output
0	0	0	0	1
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	1
0	1	0	1	0
0	1	1	0	1
0	1	1	1	0
1	0	0	0	1
1	0	0	1	1
1	0	1	0	0
1	0	1	1	0
1	1	0	0	1
1	1	0	1	0
1	1	1	0	0
1	1	1	1	0

FIG. 7

**FIG. 8**

Operation	CG	WL	WBL	V _D	V _S	M _{OUT}
Erase	V _{pp+} =12V	V _{dd} =1.8V	0V	V _{dd} =1.8V	V _{dd} =1.8V	V _{dd} =1.8V
Program	0V	V _{pp+} =12V	V _{pp} =11V	0V	0V	0V
Read	0.9V	V _{dd} =1.8V	0.9V	V _{dd} =1.8V	0V	V _{dd} or 0V

FIG. 9

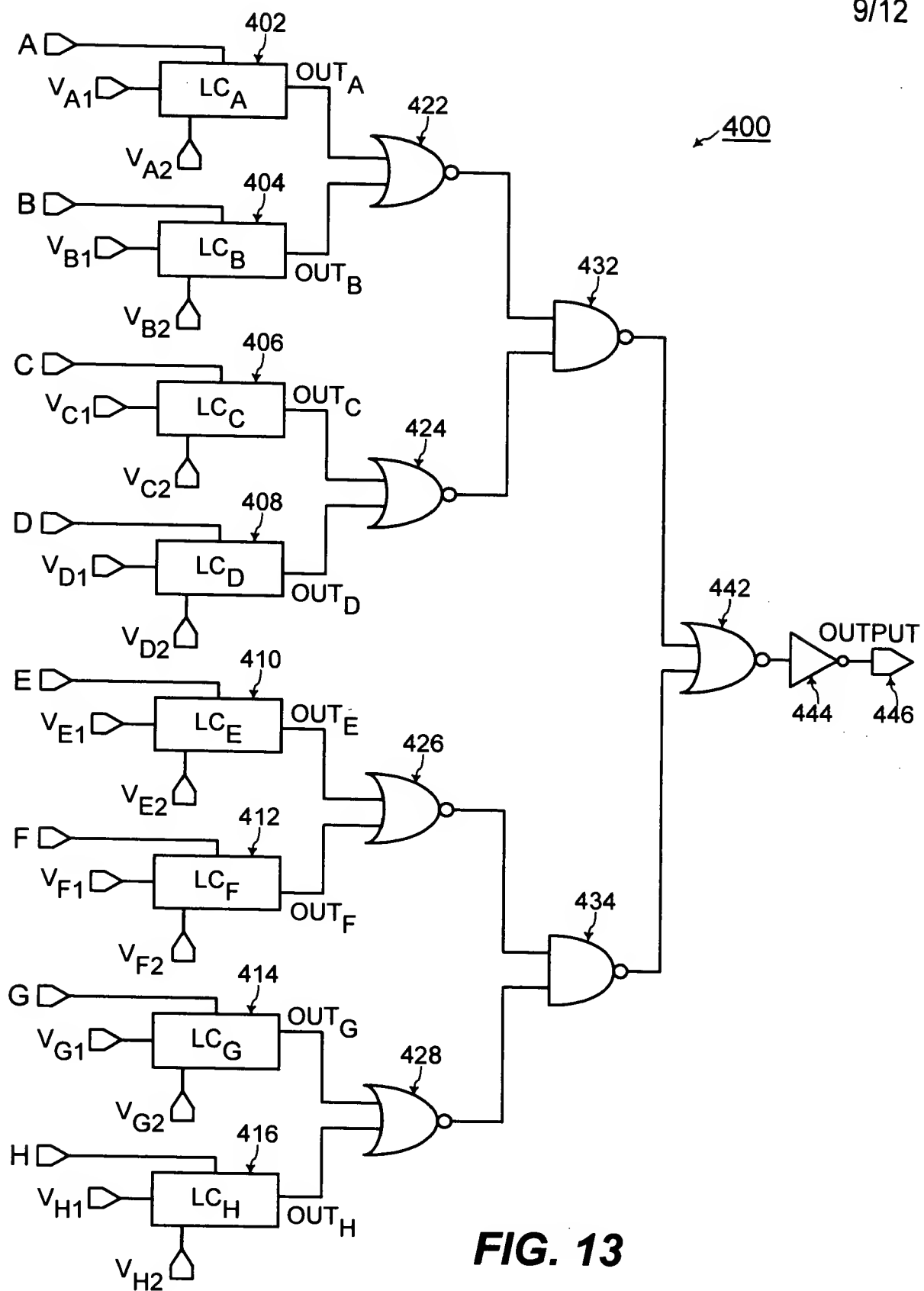
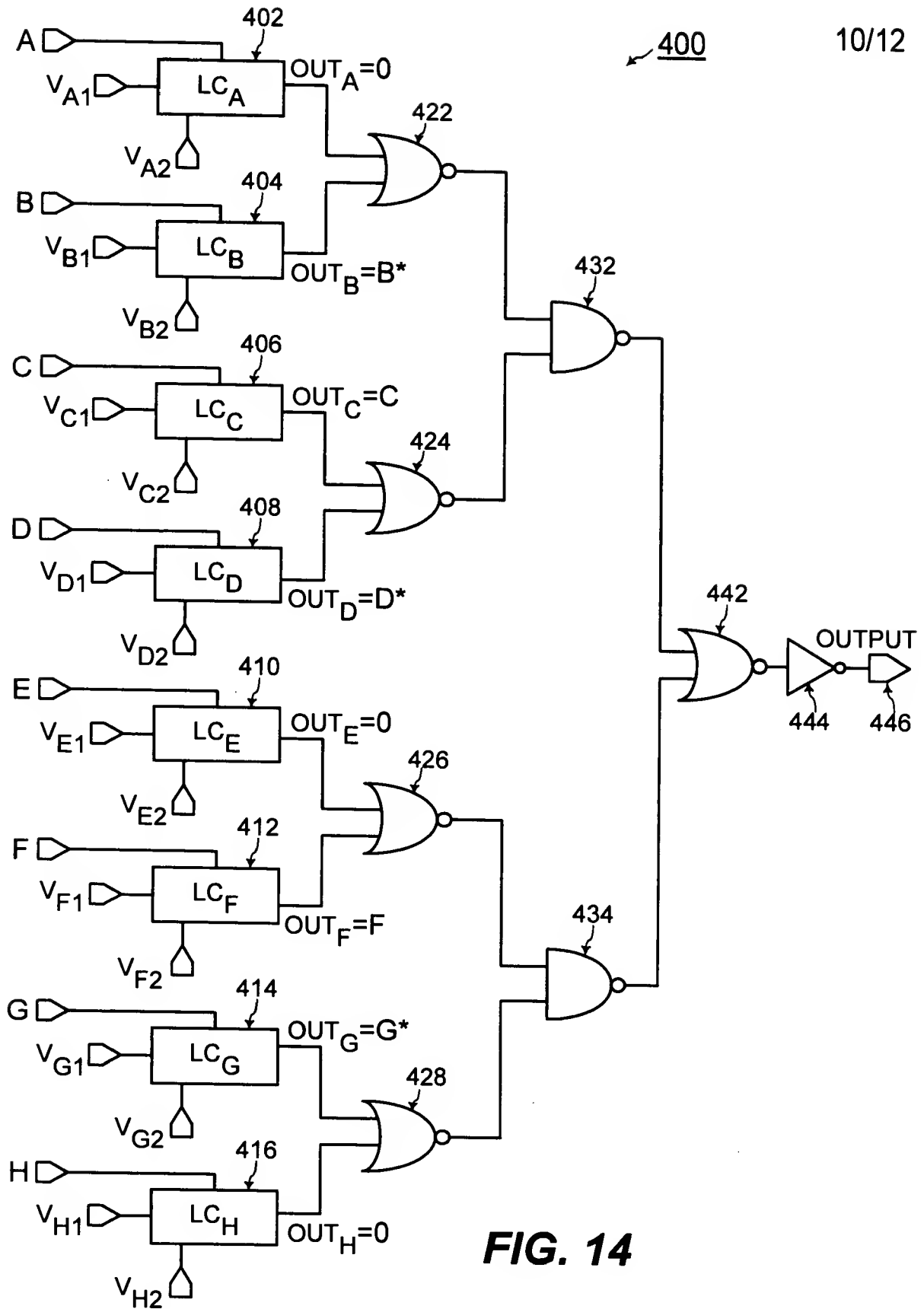


FIG. 13

09991245-11401

05991245.1.1.0.1



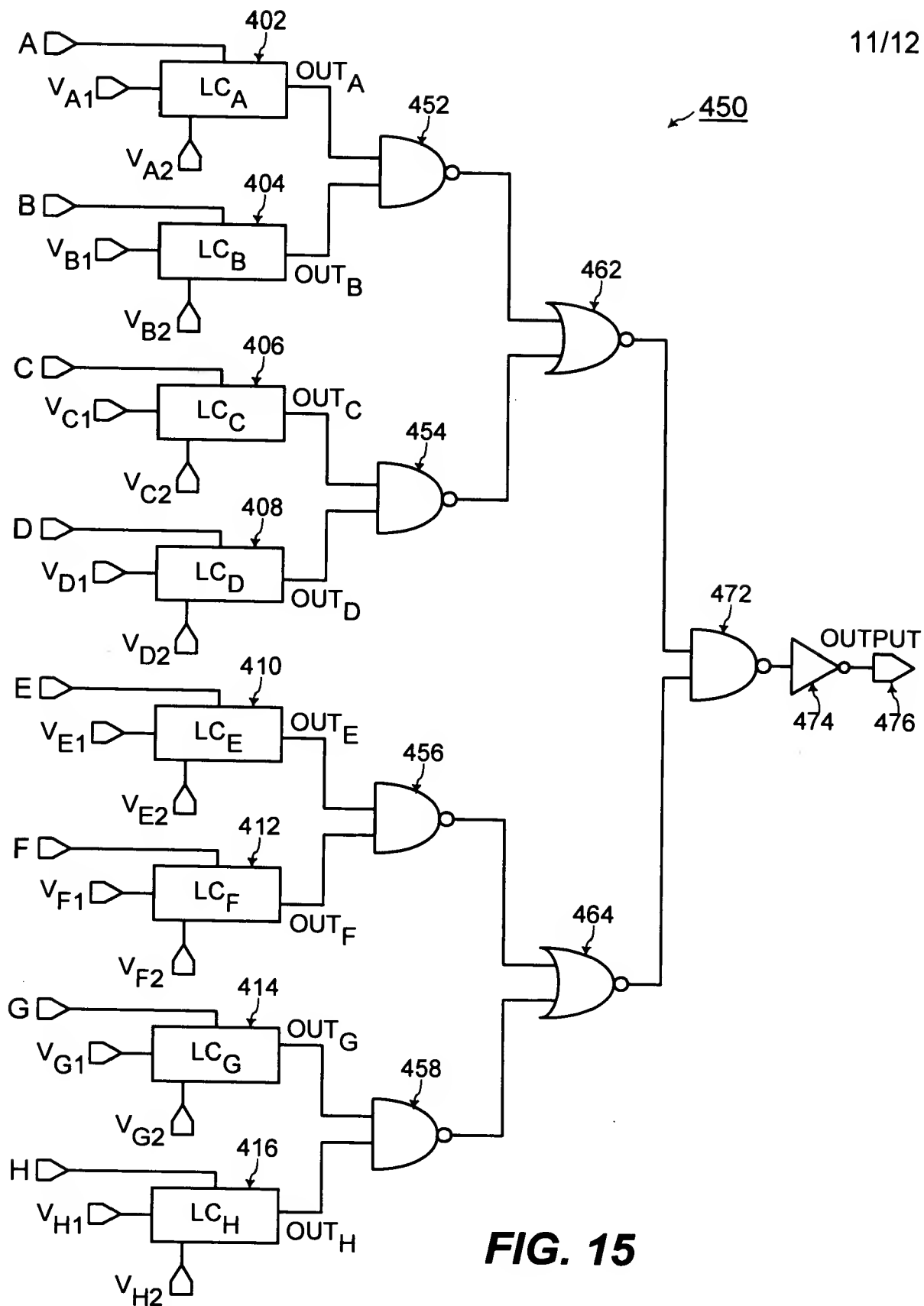


FIG. 15

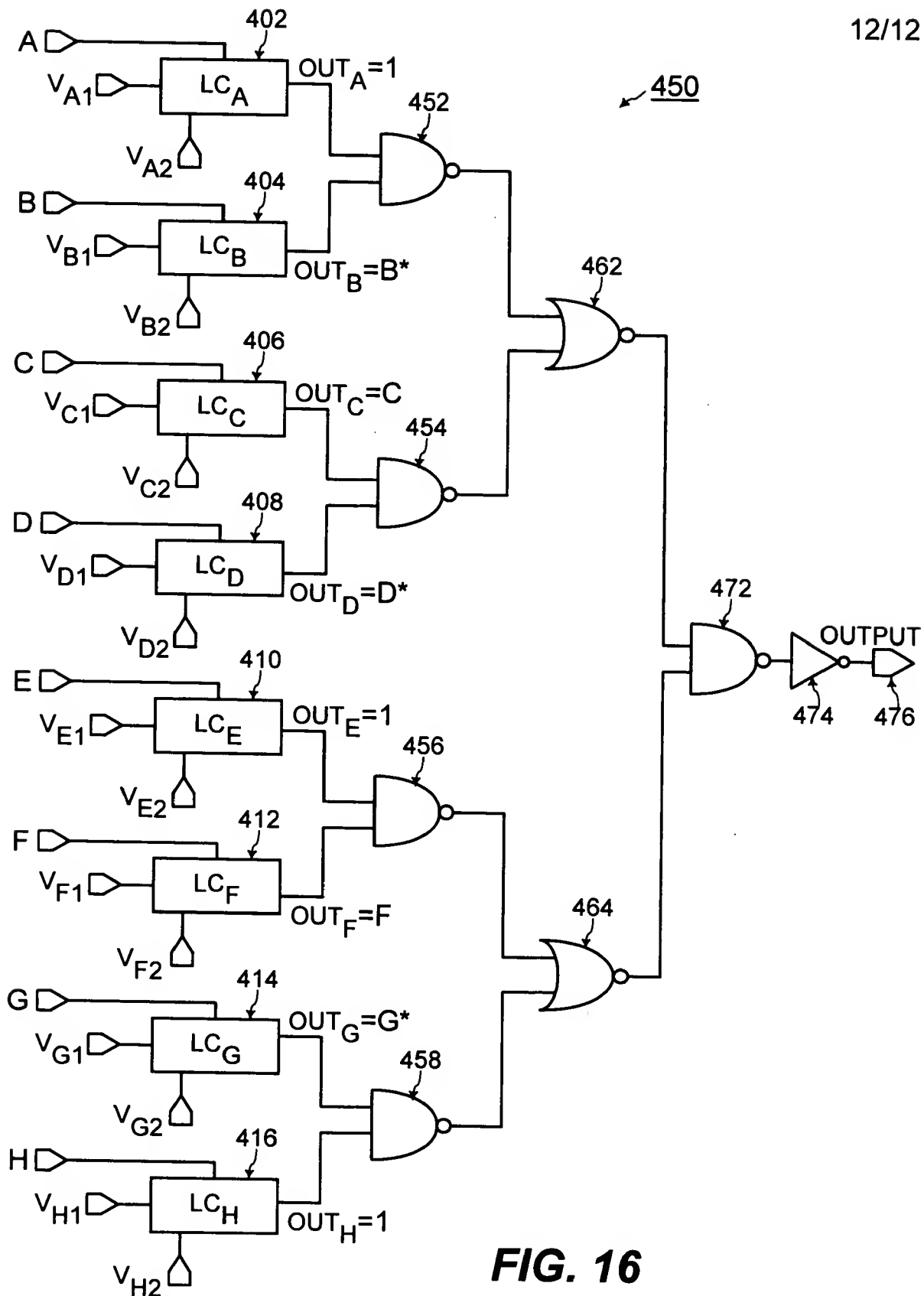


FIG. 16

0994245-11404